

10/710,001

FIS920040044US1

**REMARKS**

Claims 1 to 22 remain in the present application. Claims 2, 3 and 22 have been amended for which there is support in the specification, claims and drawings as originally filed.

Reconsideration of the Examiner's decisions and reexamination of this application are respectfully requested.

**The §112 rejections:**

Claims 2, 3, 20 and 22 have been rejected by the Examiner under 35 USC §112, second paragraph.

Claims 2 and 3 have been amended by substituting "semiconductor substrate" (in the preamble of claim 1) for "wafer".

Claim 22 has been amended by making it dependent on claim 19 and otherwise making it consistent with claim 19.

Claim 20 (dependent on claim 10) was not amended since "said implanted areas" finds sufficient antecedent basis in "implanted areas" in claim 10.

In view of the above remarks and amendments to claims 2, 3 and 22, the rejections of claims 2, 3, 20 and 22 under 35 USC §112, second paragraph, should now be overcome.

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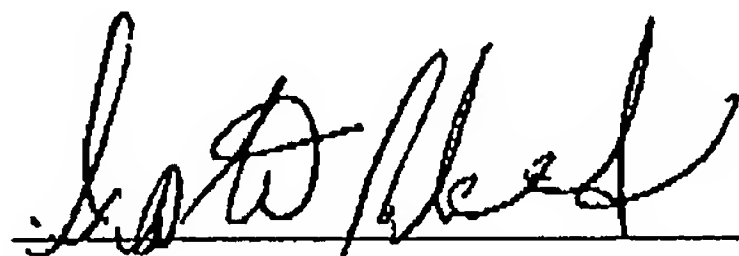
Allowable subject matter:

In view of the Examiner's indication that claims 2, 3, 20 and 22 would be allowable if rewritten to overcome the rejections of these claims under 35 USC §112, second paragraph, claims 2, 3, 20 and 22, as well as claims 6 to 9 which depend from claims 2 and 3 should be allowable. The Examiner has indicated that claims 1, 4, 5, 10 to 19 and 21 are allowed. Accordingly, all of claims 1 to 22 should be in condition for allowance.

If the Examiner finds this application deficient in any respect, the Examiner is invited to telephone the undersigned at the Examiner's earliest convenience to resolve such deficiency.

Respectfully Submitted,

Mo, et al.



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